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• Approved for use through 9/30/00. OMB 0651-0031

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TRANSMITTAL	Application / Conf. N	lo.	10/659,918 / 6014 September 11, 2003 Jason H. Anderson Unknown 2858			
TRANSMITTAL	Filing Date					
FORM	First Named Invento	or				
(to be used for all correspondence after initial filin	Examiner Name					
Mail Stop:	Art Unit					
Express Mail Receipt No.	Patent No.					
Total Number of Pages in This Submission	Attorney Docket Nui	mber	X-1396 US			
	ENCLOSUR	ES (check all that app	ply)			
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Amendment / Reply	— `	ation / Oath	´			
Preliminary Amendment	☐ Drawin	na(s)		Appeal Communication to Board of Appeals and Interferences		
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Express Abandonment Request	Г То Сог	nvert a				
P		ional Application		Other Enclosure(s) (please identify below):		
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Certified Copy of Priority Document(s)	Reque	st for Refund	İ			
Response to Missing Parts/	Remarks					
Incomplete Application						
Response to Missing Parts under 37 CFR 1.52 or 1.53						
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Attn: Justin Liu	/ //	(Customer Number)		Reg. Number 51,959		
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IN THE UNITED STATES PATENT OFFICE

icants:

Jason H. Anderson et al.

Assignee:

Xilinx, Inc.

Title:

"Leakage Power Optimization for Integrated

Circuits"

Serial No.:

10/659,918

File Date:

9/11/2003

Examiner:

Unknown

Art Unit:

2858

Docket No.:

6014

X-1396 US

Conf. No.:

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form PTO-1449A/PT. A copy of each is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfally submitted,

Justin Liu

Attorney for Applicants

Reg. No. 51,959

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Name

JL: jam

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Substitute for form 1449A/PTO				Application / Conf. No.	10/659,918 / 6014		
INFO	RMATION	N DIS	CLOSURE	Filing Date	September 11, 2003		
STATEMENT BY APPLICANT (use as many sheets as necessary)				First Named Inventor	Jason H. Anderson		
			II LIOAITI	Art Unit	2858		
			ecessary)	Examiner Name	Unknown		
Chast	1	1	1	Attorney Docket Number	V 1204 LIC		

OTHER NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No '	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
		ANDERSON, JASON H., "Active Leakage Power Optimization for FPGAs," FPGA '04, February 22-24, 2004, Monterey, California, USA, 9 pages, available from janders@eecg.toronto.edu or Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124				
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